

FIG. 1

UNITED STATES PATENT AND TRADEMARK OFFICE

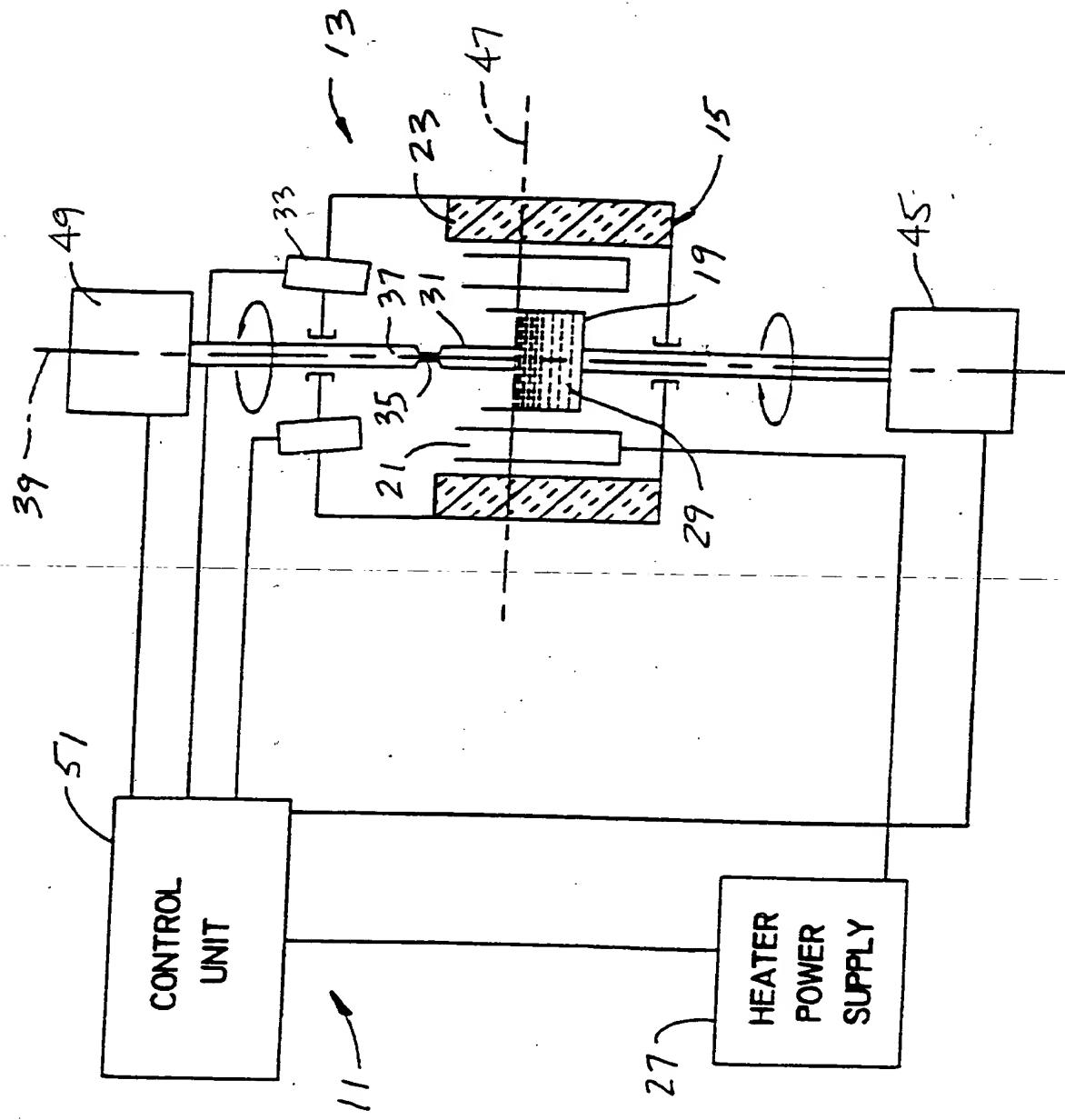


Figure 20.10.56.0

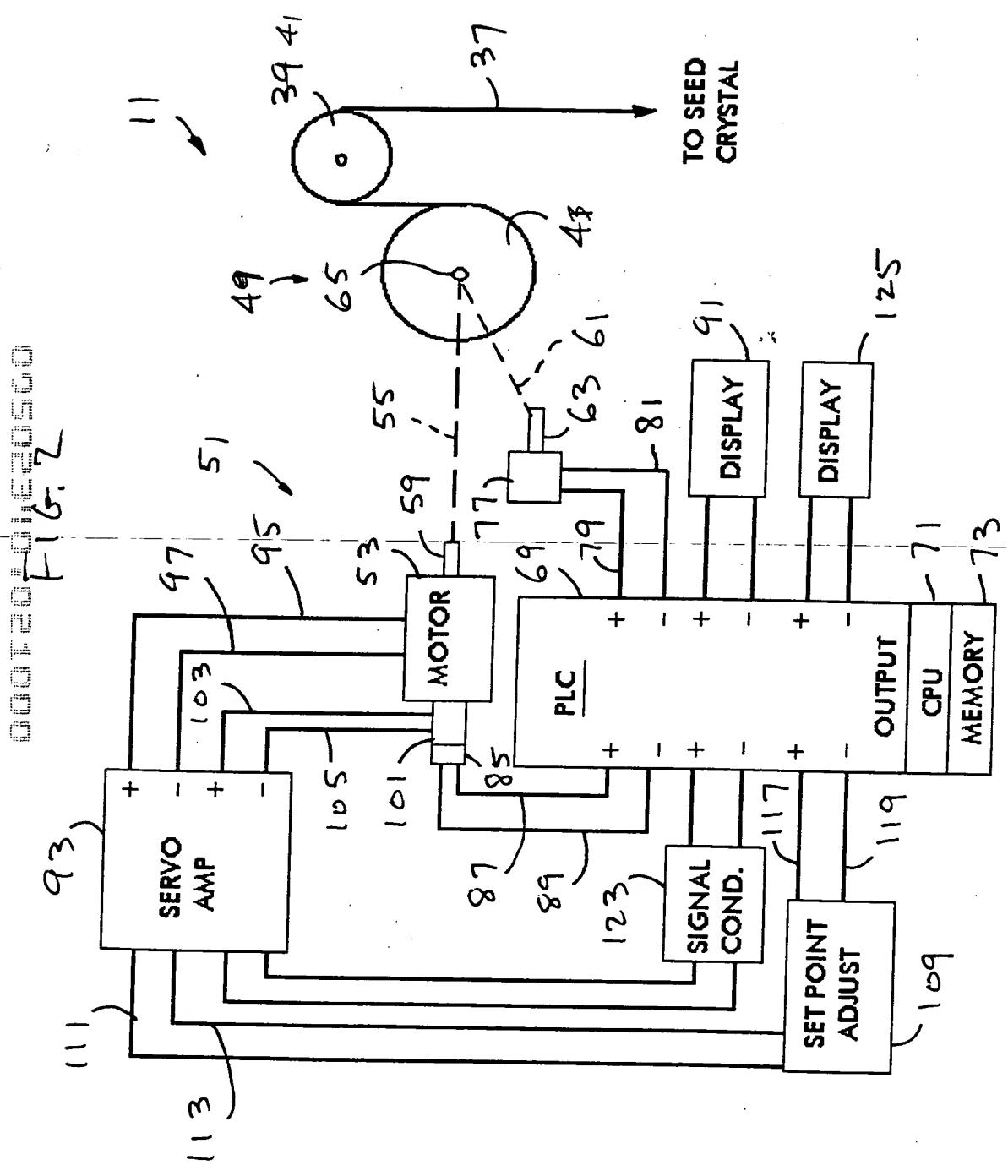
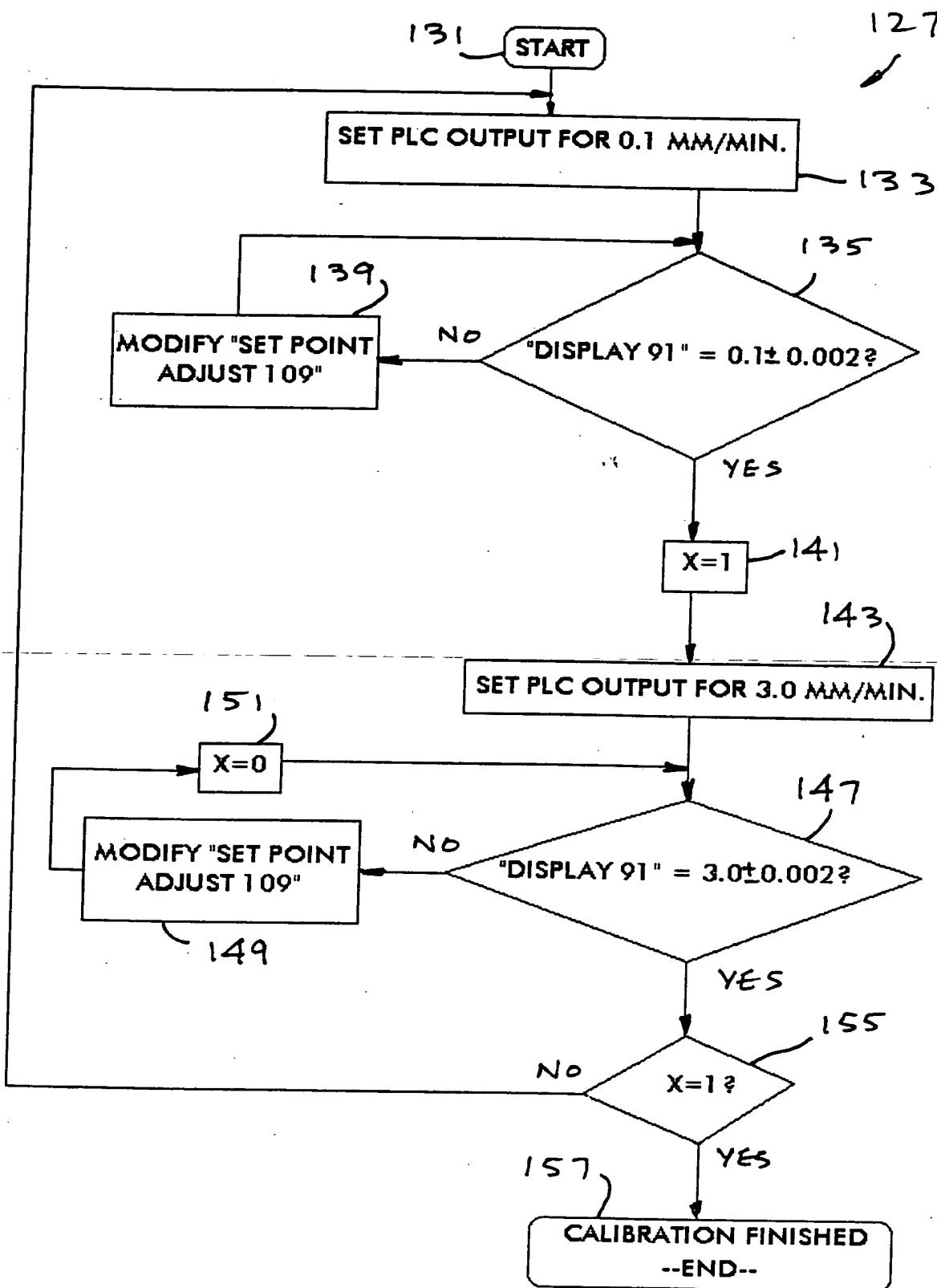


FIG.3



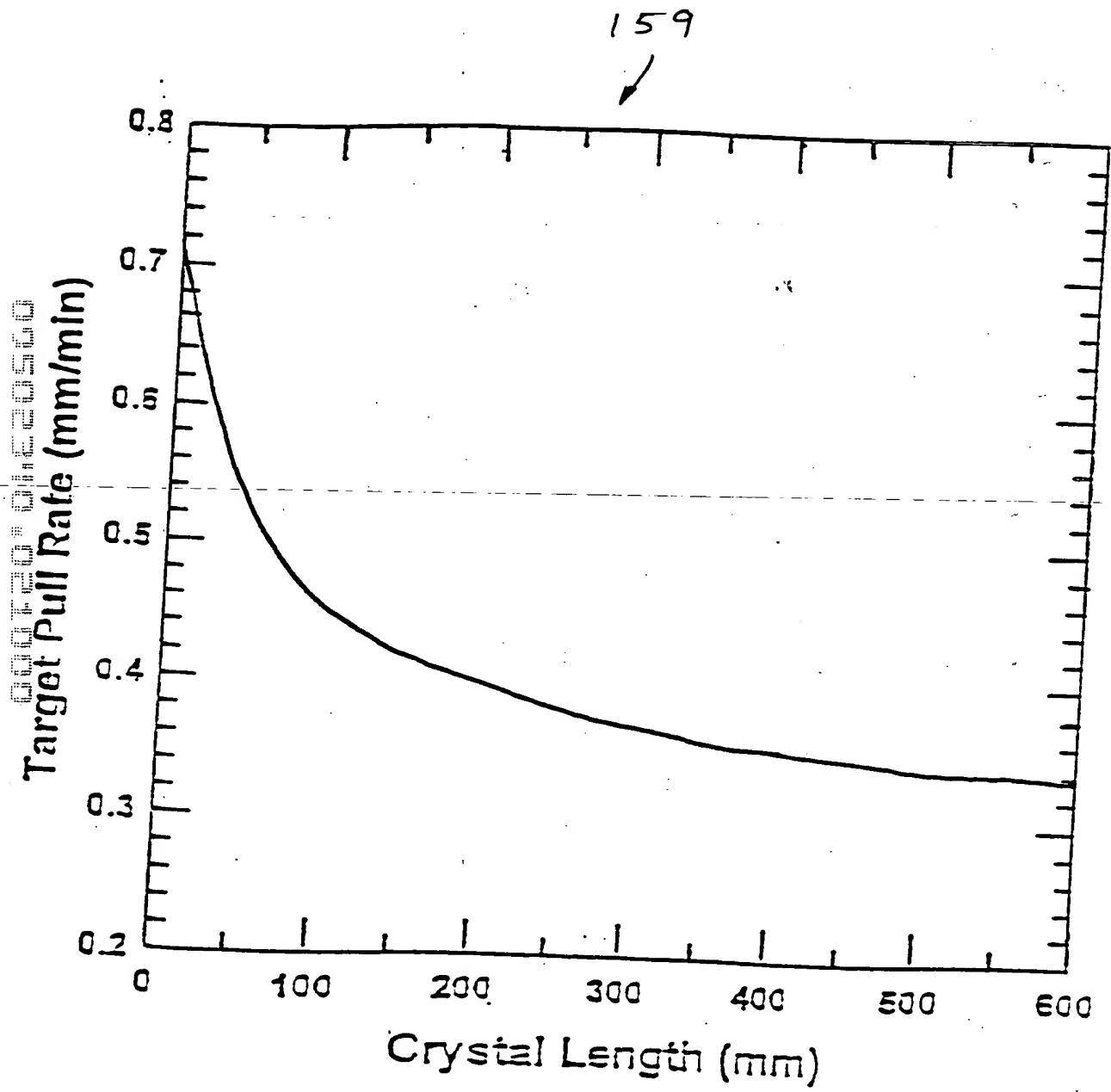


FIG. 4

FIG. 5

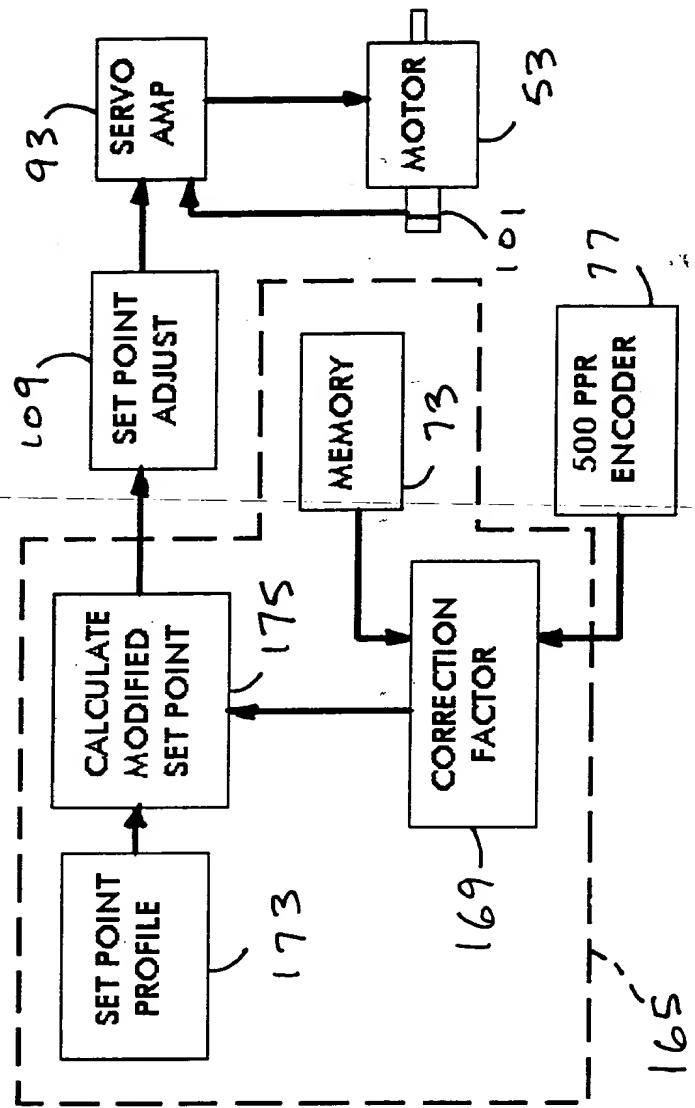


FIG. 6A

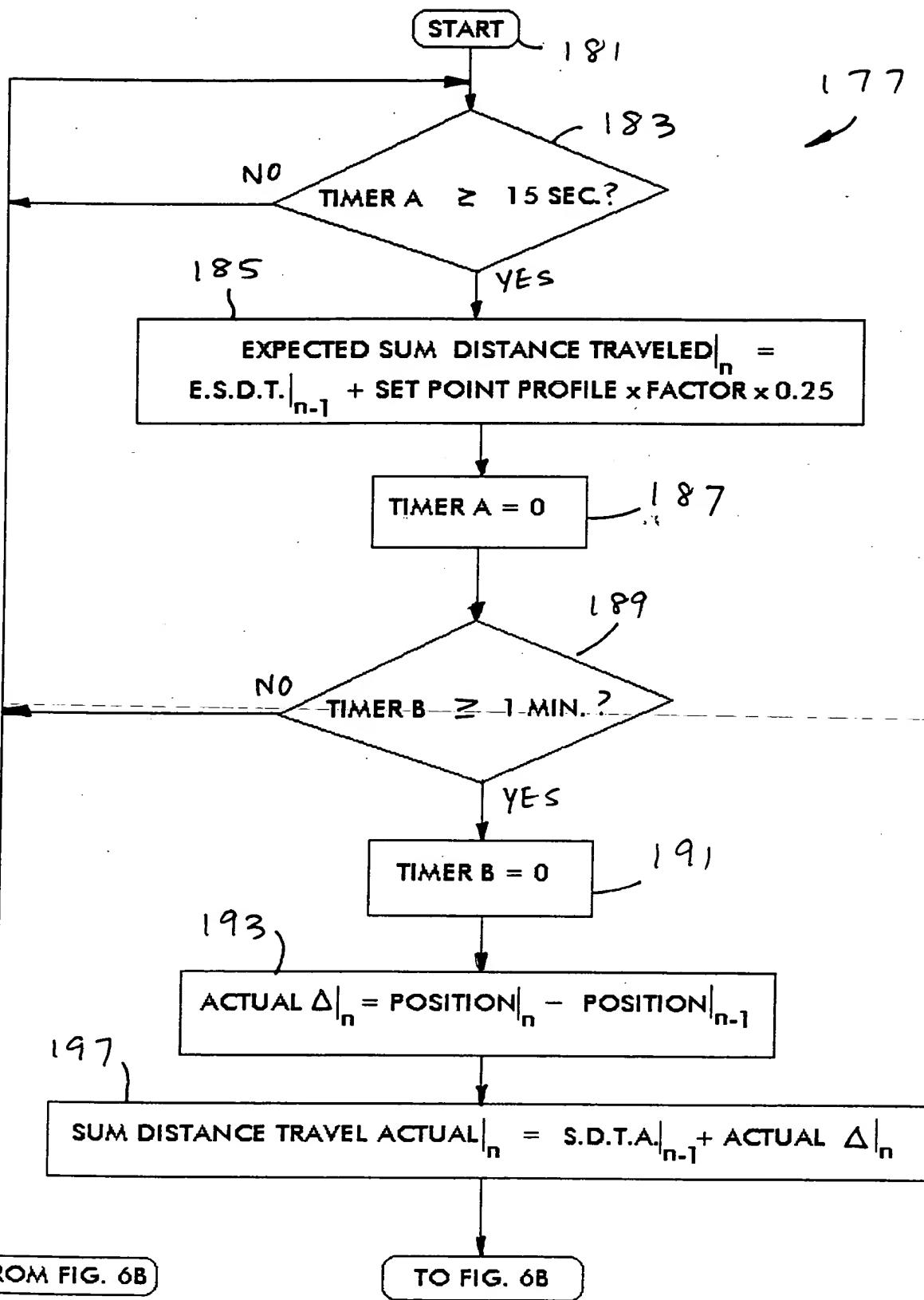
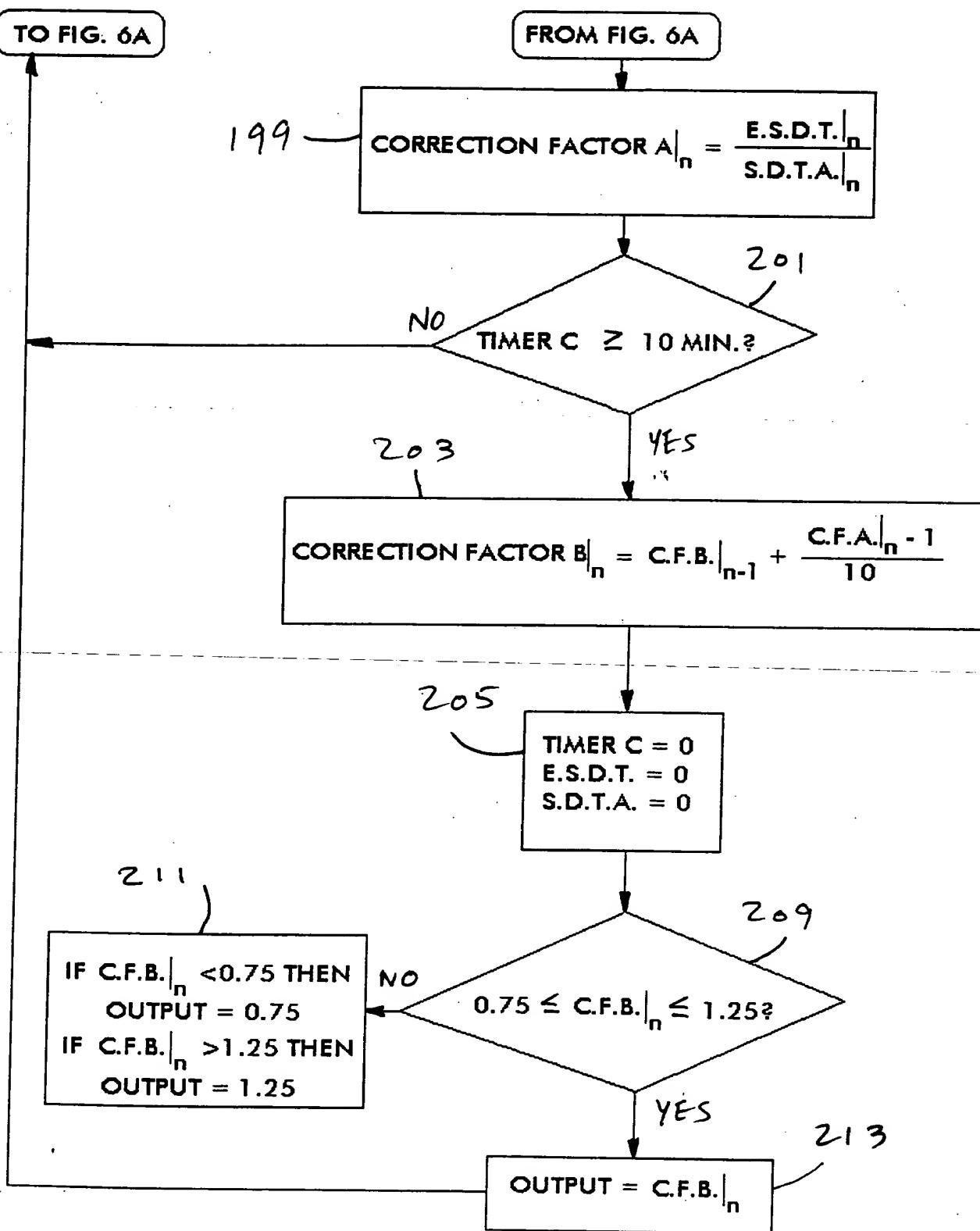


FIG. 6B



Diameter Control Loop -- Seed Lift

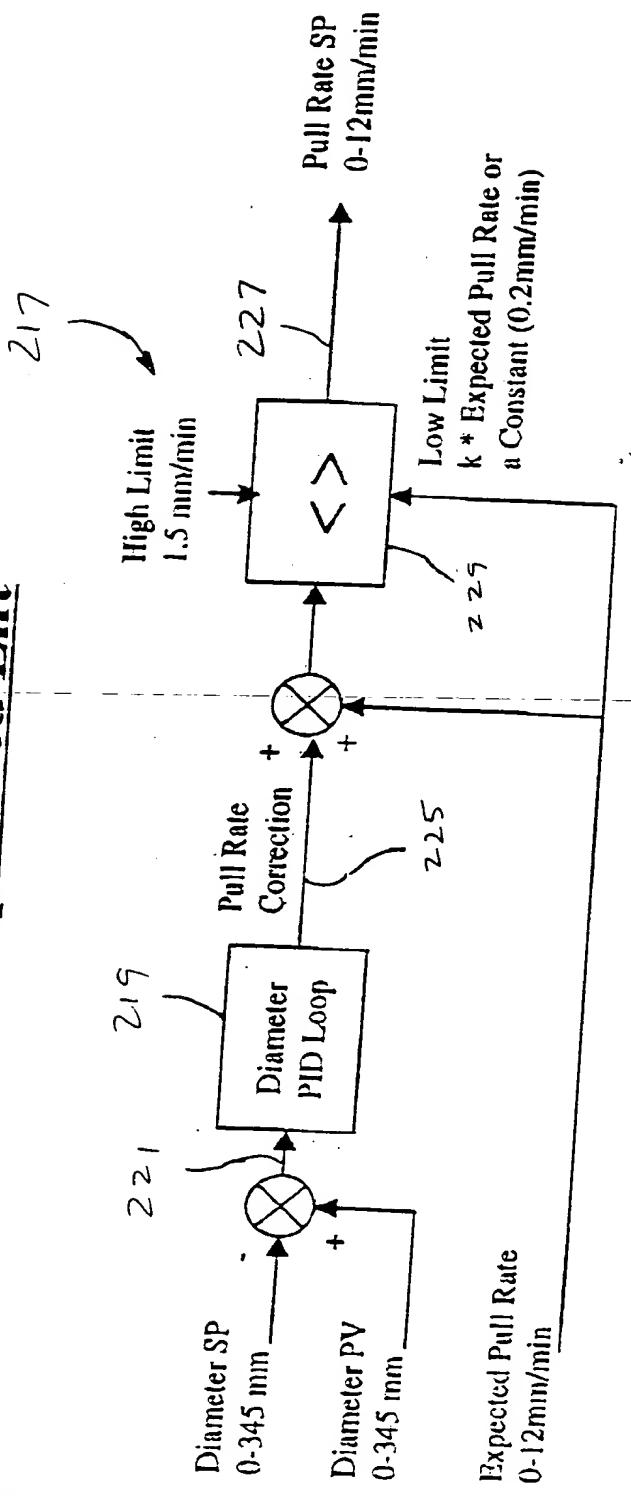
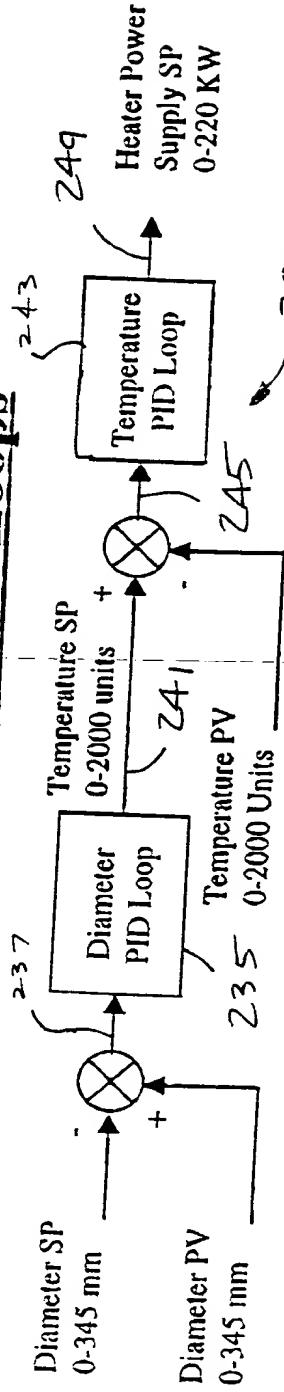


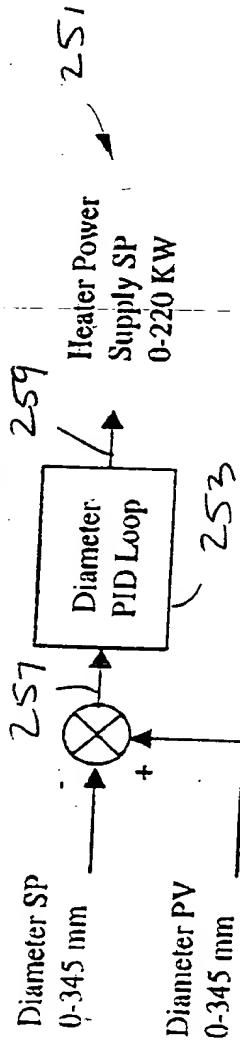
FIG. 7
PRIOR ART

Fig. 8 Power Model

Cascaded Temperature Control Loops



Power Control Loop



Power Control Loop With Process Model

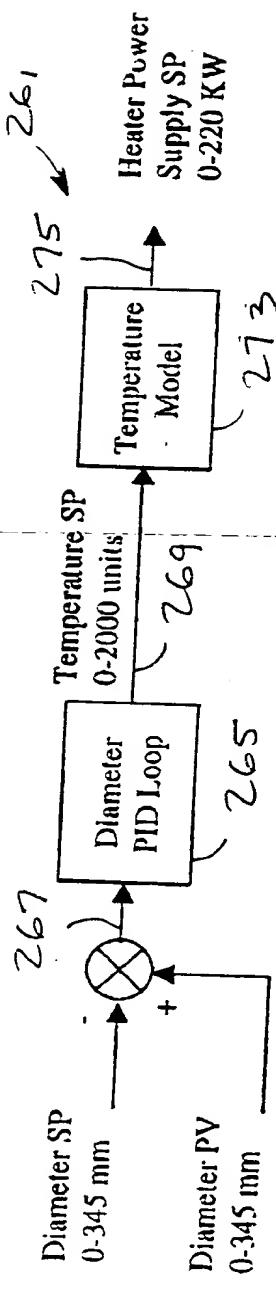
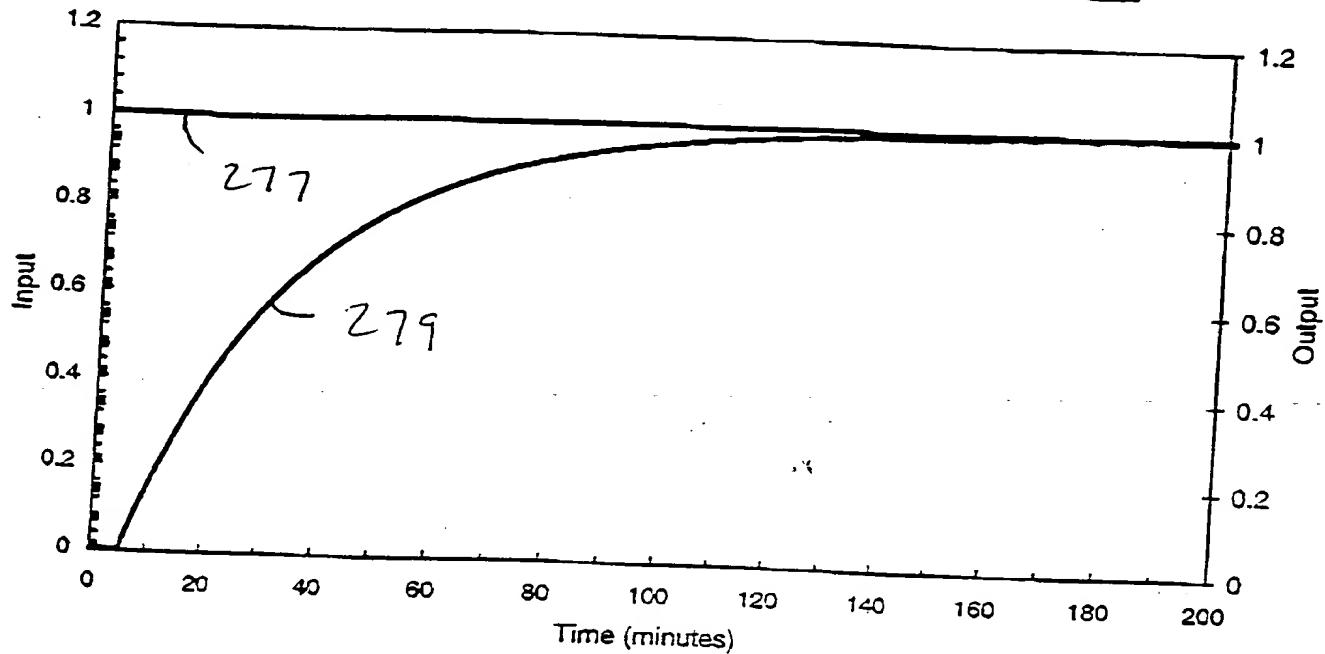


FIG. 11 PRIOR ART

First-Order Lag Function Response

(Unity Step Input at $t = 1$ Without Pulse, $\tau_u = 30$ minutes, $T_d = 5$ minutes)



First-Order Lag Function Response

(Unity Step Input at $t=1$ With 3.16 Minute, 10x Pulse, $\tau_u = 30$ minutes, $T_d = 5$ minutes)

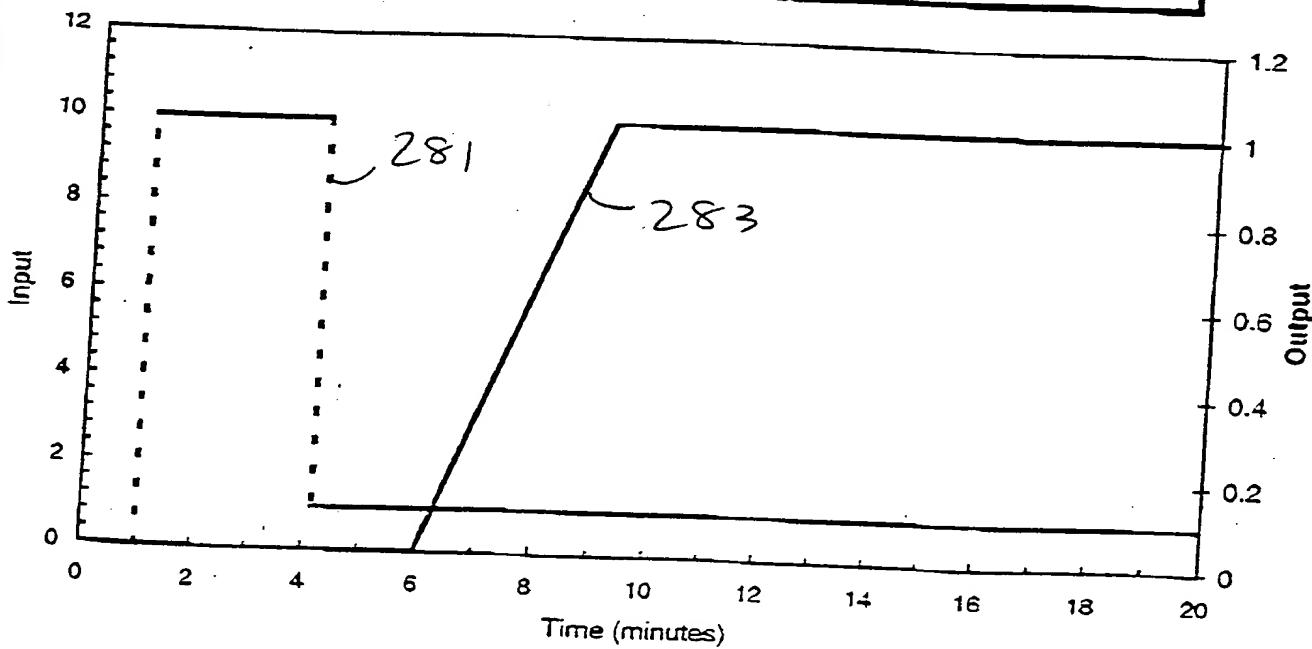


FIG. 12

FIG. 13

